

Fault Log Information

*For UDS use only

Error type 2: Provides information about whether system test passed or failed on last test cycle (and is not related to MIL being on or off). Shows “passive/ sporadic” or “active/static”.

DTC memory entry
 Number : P244000: Secondary Air Injection System Switching Valve Stuck Open Bank1
 Error type 2 : passive/sporadic
 Symptom : 17617
 Status : 01101000

☐ **Standard ambient conditions:**
 Date : 4/6/14
 Time : 8:17 AM
 Mileage (DTC) : 40394
 Priority : 2

Frequency counter : 2
 Elimination counter / driving cycle : 255

☐ **Expanded ambient conditions:**

Engine speed
 Normed load value
 Vehicle speed
 Coolant temperature
 Intake air temperature 24 °C
 Ambient air pressure 980 mbar
 Voltage terminal 30 13.405 V
 Dynamic environmental data 20 96 22 11 7D 39 11 DC 1D 11 D1 7D 11 0E 03 14 A6 82 12 03 02

Unlearning counter according OBD 34

Status: Each bit in this field contains information. The bits are numbered from left to right, starting with Bit 7:

0 1 1 0 1 0 0 0
 (Bit 7)(Bit 6)(Bit 5)(Bit 4)(Bit 3)(Bit 2)(Bit 1)(Bit 0)

- **Bit 0** contains information on the last test. If it shows “0”, the last test passed; if it shows “1”, the last test failed.
- **Bit 7** contains information on the status of the MIL. If it shows “0”, MIL is off; if it shows “1”, MIL is on. If more than one fault is stored, Bit 7 will only show “1” on the fault(s) that caused the MIL to turn on.

Frequency Counter: Counts the number of times the test fails immediately after passing (not the number of consecutive times the test failed). For example, if the test fails three times in a row, the counter stays the same. If the test passes on the fourth time, the counter stays the same. It will only go up (by one) the next time the test fails after passing:

Test #	Status	Freq. Counter	Test #	Status	Freq. Counter
1	Failed	1	6	Failed	2
2	Failed	1	7	Passed	2
3	Failed	1	8	Passed	2
4	Passed	1	9	Failed	3
5	Failed	2	10	Passed	3

Conclusion: If counter shows 1 and the MIL is on, the fault is static. The higher the counter, the more sporadic the fault.

Unlearning counter: When the fault sets, the unlearning counter shows 40. The system must then run 40 successful tests before the counter can begin to count down. After 40 successful tests, the countdown begins. So, on the 41st passing test, the counter shows 39, on the 42nd passing test, the counter shows 38, etc. When the counter reaches 0, the fault is deleted. **Conclusion:** If counter is below 39, the system is okay.

Revision #1



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